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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/553,860	10/20/2005	Norishige Nanai	10873.1744USWO	6567
53148	7590	03/29/2007	EXAMINER	
HAMRE, SCHUMANN, MUELLER & LARSON P.C. P.O. BOX 2902-0902 MINNEAPOLIS, MN 55402			GOODWIN, DAVID J	
			ART UNIT	PAPER NUMBER
			2818	
SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE		
3 MONTHS	03/29/2007	PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/553,860	NANAI ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	David Goodwin	2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 1/10/07.  
 2a) This action is FINAL.      2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 7-14 and 16-20 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 7-14 and 16-20 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____	6) <input type="checkbox"/> Other: _____

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 7 through 10 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bai (US 2004/0222412) in view of Shim (Moonsub Shim et al., "Polymer Functionalization for Air-Stable n-Type Carbon Nanotube Field-Effect Transistors", J. Am. Chem. Soc. 123, pp. 11512-11513 (2001)) in view of Takenaka (US 6,882,016).

3. Regarding claim 7.

4. Bai teaches an array comprising first and second semiconductor devices on a substrate (612) (fig 6). Said devices comprise a gate electrode (614) formed on the substrate (612) a gate insulation layer (616) formed on the gate electrode (614) a source electrode and a drain electrode (624) formed on the gate insulation layer (616). A semiconductor layer (620) formed between the source electrode and the drain electrode (624) so as to contact the source electrode and the drain electrode (624) (fig 6) (paragraph 0135). Said semiconductor comprises fullerenes (paragraphs 0089-0091).

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5. Claims directed towards the structure of the device must distinguish from the prior art based upon structural differences rather than the processes by which the product is made (MPEP 2113).

6. Bai does not teach the composition of the fullerene semiconductor material.

7. Shim teaches an n type fullerene semiconductor layer. Said n type fullerene semiconductor layer comprises nanotubes with an n type modifying polymer, polyethylene imine, on the surface thereof (paragraphs 3-6). Further Shim teaches a p type fullerene semiconductor material comprising a nanotube material (paragraphs 3-6).

8. It would have been obvious to one of ordinary skill in the art to use nanotubes to form p type TFTs and nanotubes with polyethylene imine on the surface thereof to make n type TFTs in order to obtain a reduced device size.

9. Bai in view of Shim does not teach that a first semiconductor device comprises an n type FET and a second semiconductor device comprises a p type FET.

10. Takenaka teaches an array which comprising a first semiconductor device which comprises an n type FET and a second semiconductor device which comprises a p type FET (fig 15, 16) (column 18 lines 5-67).

11. It would have been obvious to one of ordinary skill in the art to incorporate n type FETs and p type FETs on a substrate so that the devices can be controlled enabling current to be supplied to specific elements.

12. Regarding claim 8.

13. Shin teaches that the modifying polymer is an imine nitrogen containing polymer, polyethylene imine (paragraph 4).

14. Regarding claim 9.
15. Shin teaches that the modifying polymer is a polyalkyne imine, polyethylene imine (paragraph 4).
16. Regarding claim 10.
17. Shin teaches that the modifying polymer is a polyethylene imine (paragraph 4).
18. Regarding claim 12.
19. Claims directed towards the structure of the device must distinguish from the prior art based upon structural differences rather than the processes by which the product is made (MPEP 2113).
20. Claims 11, 13, 14, and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bai (US 2004/0222412) in view of Shim (Moonsub Shim et al., "Polymer Functionalization for Air-Stable n-Type Carbon Nanotube Field-Effect Transistors", J. Am. Chem. Soc. 123, pp. 11512-11513 (2001)) in view of Takenaka (US 6,882,016) as applied to claim 7 above, and further in view of Sato (US 6,002,462).
21. Regarding claim 11
22. Bai in view of Shim in view of Takenaka teaches elements of the claimed invention above.
23. Bai in view of Shim in view of Takenaka does not teach the formation of a resin protective layer over the device.
24. Sato teaches an acrylic resin protective layer over a TFT array (column 8 lines 25-45).

25. It would have been obvious to one of ordinary skill in the art to form a protective resin layer over the device in order to protect the device from environmental contamination and damage.

26. Regarding claim 13

27. Sato teaches an acrylic resin protective layer over a TFT array (column 8 lines 25-45). Acrylic resin does not contain imine nitrogen.

28. It would have been obvious to one of ordinary skill in the art to form a protective resin layer over the device in order to protect the device from environmental contamination and damage.

29. Regarding claim 14

30. Sato teaches an acrylic resin protective layer over a TFT array (column 8 lines 25-45). Acrylic resin does not contain imine nitrogen.

31. It would have been obvious to one of ordinary skill in the art to form a protective resin layer over the device in order to protect the device from environmental contamination and damage.

32. Claims 16 through 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dodabalpur (US 6,278,127) in view of Shim (Moonsub Shim et al., "Polymer Functionalization for Air-Stable n-Type Carbon Nanotube Field-Effect Transistors", J. Am. Chem. Soc. 123, pp. 11512-11513 (2001)) in view of Bai (2004/0222412).

33. Regarding claim 16 and 20.

34. Dodabalpur teaches a method of making a semiconductor device. Said method comprises forming a gate electrode (14) formed on a substrate (11). Forming a gate

insulation layer (15) formed on the gate electrode (14). Forming a source electrode (12) and a drain electrode (13) that are formed on the gate insulation layer (15). An n type semiconductor layer (21) formed between the source electrode (12) and the drain electrode (13) so as to contact with the source electrode (12) and the drain electrode (13) (fig 2) (column 5 lines 5-45). Said semiconductor comprises fullerenes (column 3 lines 45-60).

35. Dodabalpur does not teach the composition of the fullerene semiconductor layer.

36. Shim teaches a method of making an n type fullerene semiconductor layer. Said method of making n type fullerene semiconductor layer comprising nanotubes wherein an n type modifying polymer, polyethylene imine, is applied to a portion of the surface thereof only where the layer should be converted to a n type semiconductor (paragraphs 3-6). Thereby converting p type semiconductor layer into n type semiconductor layer, only where it should be changed is it changed.

37. It would have been obvious to one of ordinary skill in the art to use an n type fullerene comprising nanotubes with n type modifying polymer thereon in order to obtain an air stable n type semiconductor.

38. Dodabalpur in view does not teach that the n type modifying polymer is applied by an ink jet dispensing method.

39. Bai teaches that the polymers for making an organic TFT are dispensed by an ink jet method (paragraph 0043).

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40. It would have been obvious to one of ordinary skill in the art to dispense the polymers using an ink jet method in order to minimize the amount of polymer used in the process.

41. Regarding claim 17

42. Shin teaches that the modifying polymer is an imine nitrogen containing polymer, polyethylene imine (paragraph 4).

43. Regarding claim 18.

44. Shin teaches that the modifying polymer is a polyalkyne imine, polyethylene imine (paragraph 4).

45. Regarding claim 19.

46. Shin teaches that the modifying polymer is polyethylene imine (paragraph 4).

***Response to Arguments***

47. Applicant's arguments filed 1/10/2007 have been fully considered but they are not persuasive.

48. The applicant argues that the prior art does not teach forming the semiconductor regions at the same time.

49. Claims directed towards the structure of the device must distinguish from the prior art based upon structural differences rather than the processes by which the product is made (MPEP 2113).

50. The applicant argues that Taneka does not teach the use of carbon nanotubes as a semiconductor.

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51. In response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986).

52. The applicant argues that the prior art teaches a pn junction and not a field effect transistor.

53. In response to applicant's arguments, the recitation of a field effect transistor has not been given patentable weight because the recitation occurs in the preamble. A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

### ***Conclusion***

54. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within

TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Goodwin whose telephone number is (571)272-8451. The examiner can normally be reached on Monday through Friday, 9:00am through 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (571)272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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DJG

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